

The Host Interoperability Support Test (HIST) Result



Manufacturer: Neles Finland Oy
MANUFAC_ID: 0x000E05
Device Name: VG9000F
DEV_TYPE: 2329
DEV_REV: 0x03
DD_REV: 0x02
Capability Level (If supported):
CFF File Name: 030101.cff
DD File Name (DDv4/DDv5): 0302.ff5, 0302.sy5, 0302.ffa, 0302.sym
DEVICE_ID: 000E052329VG90001251164830
Firmware Revision
Parameter Name: VC_SOFTWARE_REVISION (Resource Block)
Firmware Revision Number: F18288-1.44

FF-569 Section	Feature	Content	Result		
3.1.1	H1 Device Address Assignment	1. Assign a permanent device address 2. Clear a device address		PASS	
3.1.2	Configuration of Link Master Devices	1. Support configuration of Link Master parameters including: ConfiguredLinkSettings (Slot Time, Minimum Inter-PDU Delay, Max Response Delay, etc) and PrimaryLinkMasterFlag LAS Scheduling		PASS	
3.1.3	H1 Physical Device Tag Assignment	1. Assign a PD-Tag 2. Clear a PD-Tag		PASS	
3.1.4	Convert Link Master to Basic Device	1. Convert Device Class to BASIC 2. Convert Device Class to Link Master		PASS	
3.1.5	H1 Software Download	1. Execute software download 2. Download function blocks configuration to the device	NOT RUN*		
3.3.1	Block Tag Configuration	1. Set Block Tag name for Resource Block, Transducer Block and Function Blocks		PASS	
3.3.2	Block Instantiation	1. Instantiate a block 2. Delete an instantiated block	NOT RUN*		
3.3.3	Multiple Capability Levels	1. Select a capability level for offline configuration 2. Download the function blocks configuration to the device	NOT RUN*		
3.3.4	Resource and Transducer Blocks	1. Read/Write all parameters of Resource and Transducer Blocks		PASS	
3.3.5	Standard Function Blocks	1. Read/Write all parameters of all Standard Blocks and the standard portion of Enhanced Blocks		PASS	
3.3.6	Enhanced Function Blocks	1. Read/Write all parameters of all Enhanced Blocks	NOT RUN*		
3.3.7	Profiled Custom Function Blocks	1. Read/Write all parameters of all Profiled Custom Blocks	NOT RUN*		
3.3.8	Custom Function Blocks	1. Read/Write all parameters of all Custom Blocks	NOT RUN*		
3.3.9	Configuration of scheduled control function blocks	1. Configure a distributed application included a PID closed loop control across multiple field devices without host-based function blocks	NOT RUN*		
3.3.10	Function Block Linking and Publication Scheduling	1. Link Function Blocks		PASS	
3.3.11	Function Block Execution Scheduling	1. Schedule Function Blocks		PASS	
3.3.16	Use Views for Block Detail Reads	1. Access parameters with device Views		PASS	
3.5.1	DD Block and Parameters	1. Import a manufacturer's DD 2. Display all the parameter labels		PASS	
3.5.9	Capability File Support	1. Import a capability file for offline configuration		PASS	
3.4.1	Process Alert Management Configuration	1. Configure device to source alert objects		PASS	

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3.4.2	Process Alert Handling and Confirmation	1. Simulate a Process Alert 2. Confirm the Process Alert message (via FMS Acknowledge Service)		PASS	
3.4.3	Device Alert Management Configuration	1. Configure device to source alert objects		PASS	
3.4.4	Device Alert Handling and Confirmation	1. Simulate a Device Alert 2. Confirm the Device Alert message (via FMS Acknowledge Service)		PASS	
3.6.4	Data Quality Support through Host Controller Connections	1. React to statuses of good, bad, uncertain, and limited 2. React to cascade initialization and handshaking within a host controller	NOT RUN*		
-	T1.1 Fault State Handling	1. Configure fault state values 2. Confirm fault state handling			FAIL(See Note 1)
-	T1.2 Support for Remote CAS Mode	1. Configure Remote CAS strategy 2. Confirm Remote CAS handling	NOT RUN*		
-	T1.3 Server Stress	1. Decrease macrocycle as short as possible		PASS	
-	T1.4 Backup Link Master with Multiple sub-schedules	1. Claim LAS 2. Transfer LAS		PASS	
-	T1.5 Multiple sub-schedules	1. Configure multiple sub-schedules 2. Change LAS		PASS	
-	T1.6 DD Menu & Method	1. Open DD Menu 2. Execute DD Method		PASS	
-	T1.7 Residual Activity (Health Check)	1. Confirm Residual Activity is transmitted regularly		PASS	
-	T1.8 Bus Traffic Stress and Continuous	1. Continuous Test		PASS	

Final Verdict	AVAILABLE WITH RESTRICTION
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NOTE1	As per the test case specification during Fault state, the FSTATE_VALUE_Dx should be reflected in IN_Dx. Here in this test after ALF disconnection, the FSTATE_VALUE_Dx is not reflected in IN_Dx value. As per vendor, the current firmware implementation does not support the IN_Dx change to FSTATE_VALUE_Dx. Only the parameter FSTATE_STATUS shows that points are in Fault STATE.
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TEST ENVIRONMENT

TEST INFO	
Site Location	HIST Laboratory at SGDC (Singapore)
Test Period (Date)	23/Jun/2020 – 02/Jul/2020
Test Officer	Payal Agarwal
Supervisor	Vidyaprasad Anbaruvi
Device Vendor Representative	Johanna Järvinen <johanna.jarvinen@neles.com>
SYSTEM INFO	
Host Name (Revision)	CENTUM VP R6.07.00, PRM R4.03.01
H1 Interface Module (Revision)	ALF111 R20
Test Procedure Document Revision	Foundation TM fieldbus Interoperability Test Bed Procedures for Integrated Host CENTUM VP Rev 1
	Foundation TM fieldbus Addendum Interoperability Test Procedures for Integrated Host CENTUM VP Rev 1
HIST Report Document Number	
Host Interoperability Support Test Report	SSD-NDFB91-D0392 Revision 2

(*) **NOT RUN:** The unperformed test item due to the features which the device does not support.